

**What Is Claimed Is:**

1. An LCD device comprising:

a plurality of data pads;

an LCD panel defined by a plurality of pad regions;

a first shorting bar connected to odd numbered data pads among the plurality of data pads;

a second shorting bar connected to even numbered data pads among the plurality of data pads; and

a test pad formed in a predetermined portion of a pad region among the plurality of pad regions to apply a signal voltage for on/off testing to the first shorting bar and the second shorting bar.

2. The LCD device as claimed in claim 1, wherein the test pad includes:

a first conductive pattern connected to the first shorting bar;

a second conductive pattern connected to the second shorting bar; and

a transparent conductive film electrically connecting the first conductive pattern to the second conductive pattern.

3. The LCD device as claimed in claim 2, wherein the first and second conductive patterns are disposed with a gate insulating film interposed therebetween.

4. The LCD device as claimed in claim 3, wherein the first conductive pattern is formed on a substrate, and the second conductive pattern is formed on the gate insulating film.

5. The LCD device as claimed in claim 2, further comprising a passivation film formed on an entire surface of a substrate including the second conductive pattern, the passivation film including a contact hole that exposes predetermined portions of the first conductive pattern and the second conductive pattern.

6. The LCD device as claimed in claim 1, wherein the first shorting bar and the second shorting bar are disposed with a gate insulating film interposed therebetween.

7. The LCD device as claimed in claim 6, wherein the first shorting bar is formed on a same layer as a first conductive pattern, and the second shorting bar is formed on a same layer as a second conductive pattern.

8. The LCD device as claimed in claim 1, further comprising:

a plurality of gate pads;

a third shorting bar connected to odd numbered gate pads among the plurality of gate pads; and

a fourth shorting bar connected to even numbered gate pads among the plurality of gate pads.

9. The LCD device as claimed in claim 8, wherein a test pad applying a signal voltage to the third shorting bar is formed in a different portion than a test pad applying a signal voltage to the fourth shorting bar.

10. An LCD device comprising:

a plurality of data pads;

an LCD panel defined by a plurality of pad regions;

a first shorting bar connected to data pads that drive a first color;

a second shorting bar connected to data pads that drive a second color;

a third shorting bar connected to data pads that drive a third color; and

a test pad formed in a predetermined portion of a pad region among the plurality of pad regions to apply a signal voltage for on/off testing to the first shorting bar and the second shorting bar.

11. The LCD device as claimed in claim 10, wherein the test pad includes:

a first conductive pattern connected to the first shorting bar;

a second conductive pattern connected to the second shorting bar;

a third conductive pattern connected to the third shorting bar; and

a transparent conductive film electrically connecting the first shorting bar, the second shorting bar, and the third shorting bar to one another.

12. The LCD device as claimed in claim 10, wherein the first shorting bar and the second shorting bar are disposed with a gate insulating film interposed therebetween.

13. The LCD device as claimed in claim 10, wherein the second shorting bar and the third shorting bar are disposed with a gate insulating film interposed therebetween.

14. The LCD device as claimed in claim 11, wherein first conductive pattern and the second conductive pattern are disposed with a gate insulating film interposed therebetween.

15. The LCD device as claimed in claim 11, wherein first conductive pattern and the third conductive pattern are disposed with a gate insulating film interposed therebetween.

16. The LCD device as claimed in claim 11, further comprising a passivation film formed on an entire surface of a substrate including the third conductive pattern, the passivation film including a contact hole that exposes predetermined portions of the first conductive pattern, the second conductive pattern, and the third conductive pattern.

17. The LCD device as claimed in claim 11, wherein the third conductive pattern is electrically connected to the third shorting bar by the transparent conductive film penetrating a passivation film and a gate insulating film.